

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. OHYANAGI, et al.
Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial
No. 10/393,951, filed March 24, 2003
Filed: On Even Date Herewith
For: SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD THEREOF
Art Group of Parent: 2815
Examiner of Parent: A. R. Wilson

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 4, 2004

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Specification; and

Remarks are included following the amendments.